

**BTC COMESH VALUE PROPOSITIONS**  
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***PROBLEMS SOLVED UNIQUELY BY BTC PRODUCTS***

**Maintaining competitive edge through low cost of ownership**

Superior functionality

- 300 MHz guaranteed for five levels of logic
- automated synthesis/place&route turn-around within hours
- integrated verification to reduce risk in design changes

More user logic resources for the same cost

- easier and less expensive design integration
- more efficient synthesis and layout for better resource utilization
- sufficient available logic to accommodate almost any user design

**Maintaining competitive edge through faster time-to-market**

Fewer, faster design iterations

- automated timing closure
- stable timing behavior even as the design changes
- integrated verification prevents design change errors
- preserves reliability of delivery schedules

Fewer engineering hours to reach design goals

- incremental design development
- automated test bench validation
- complete, one-step integrated tool chain  
from functional verification to functioning hardware

## **Maintaining competitive edge through superior ease-of-use**

Automated circuit generation, synthesis, placement and routing

- designs generated automatically from functional specification
- designs generated automatically to meet performance specifications
- no additional hand crafted layout work
- no specialized hardware expertise required

Full compatibility

- netlist in, netlist out
- integrates with conventional tool chain whenever desired
- handles all types of logic with equal efficiency
- locked-down pin assignment does not affect timing or logic capacity
- pin-compatible products

Superb engineering support

- easier trouble-shooting through hardware/software co-design
- readily available standard parts, rapid product delivery

## ***PROBLEMS ADDRESSED BY COMMERCIAL TECHNIQUES AVAILABLE TO BTC***

Functionality

- eight or more clock domains
- internal diagnostics and debugging
- compliant to PCI and other bus protocols
- compliant for almost all i/o protocols and standards
- wide diversity of available IP cores